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## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Jean-Michel Reynes GROUP ART UNIT: 2818  
APPLN. NO.: 09/715,748 EXAMINER: Renee R. Berry  
FILED: November 17, 2000  
TITLE: METHOD OF FORMING A DIODE FOR INTEGRATION WITH A  
SEMICONDUCTOR DEVICE AND METHOD OF FORMING A  
TRANSISTOR DEVICE HAVING AN INTEGRATED DIODE

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on

12/19/03

*Elaine Cox*

Signature

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AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated October 14, 2003, Paper No. 4, and Examiner's  
comments with regard thereto, please enter the following amendments in the above-entitled  
application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made,  
the Commissioner is hereby authorized to credit or debit Deposit Account 502117.

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